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09/862,952	05/22/2001	Markus Helfenstein	60705-1610	9760

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EXAMINER

VARTANIAN, HARRY

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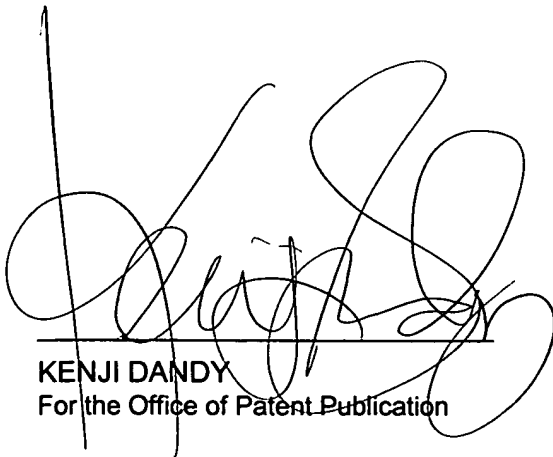
Serial No. : 09862952  
Applicant : Markus Helfenstein  
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## ACKNOWLEDGEMENT OF REQUEST

### *Notice of Allowance/Allowability Mailed*

The request for a copy of the initialed PTO 1449, dated June 27, 2005, has been received by the U.S. Patent and Trademark Office.

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P.O.-1449

## INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

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060705-1610Serial No.  
09/862,952Applicant  
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May 22, 2001Group  
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## U.S. PATENT DOCUMENTS

Examiner Initials	Item	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate

## FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation
							Yes No

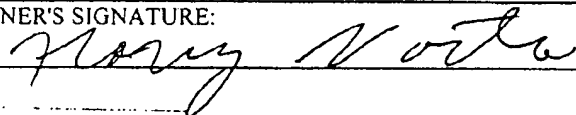
## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

H.V.	A	An Integrated Adaptive Analog Balancing Hybrid For Use In (A) DSL Modems, Frédéric Pécourt, Jörg Hauptmann, Aner Tenen, 1999 IEEE International Solid-State Circuits Conference, ISSCC99/Session 14 Paper TP 14.8, February 16, 1999, pages 252-253, 466.
	B	
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